data and common electrodes on the first substrate, the data electrode being connected to a data line and the common electrode being connected to a common line, and a distance between the electrodes being less than a thickness of the liquid crystal layer.

29. The liquid crystal display device of claim 28, wherein an alignment direction of liquid crystal molecules of the liquid crystal layer adjacent to the first substrate is parallel to the gate line.

Cont'd B1

30. The liquid crystal display device of claim 28, wherein an alignment direction of liquid crystal molecules of the liquid crystal layer adjacent to the first substrate is perpendicular to the gate line.

31. A liquid crystal display device comprising:

a first substrate;

a second substrate;

a liquid crystal layer between the first substrate and the second substrate;

a first electrode formed on a first surface of the first substrate; and

a second electrode formed on the first surface;

wherein said second electrode is formed such that a distance between the first electrode and the second electrode is less than a distance between the first substrate and the second substrate.

32. The liquid crystal display device of claim 31, wherein the first electrode has a first electrode width, the second electrode has a second electrode width approximately equal to the first electrode width, and a distance between the first electrode and the second electrode is approximately equal to the first electrode width.

33. The liquid crystal display device of claim 31, further comprising:

a first alignment layer formed on the first substrate; and

a second alignment layer formed on the second substrate.

Cont'd 31

34. The liquid crystal display device of claim 33, further comprising:

a polarizer formed on the first substrate; and

an analyzer formed on the second substrate.

35. The liquid crystal display device of claim 33, wherein the first alignment layer has a different anchoring strength from the second alignment layer.

36. The liquid crystal display device of claim 34, further comprising a thin film transistor formed between the first substrate and the first alignment layer.

37. The liquid crystal display device of claim 34, further comprising a retardation film formed on said second substrate.

38. A liquid crystal display device comprising:

a first substrate having a first alignment layer;

a second substrate having a second alignment layer;

a liquid crystal layer having a plurality of liquid crystal molecules between the first and the second substrate;

a gate line on the first substrate; and

data and common electrodes on the first substrate, the data electrode being connected to a data line and the common electrode being connected to a common line, with the distance between the electrodes being less than a thickness of the liquid crystal layer, creating a parallel electric field between the data and common electrodes with a first strength adjacent to the first substrate and a second strength adjacent to the second substrate, the first strength being higher than the second strength.

39. The liquid crystal display device of claim 38, wherein the first strength is a maximum strength.

40. The liquid crystal display device of claim 38, wherein the second strength is a minimum strength.

41. The liquid crystal display device of claim 38, wherein the first strength is a maximum strength and the second strength is a minimum strength.